



<b>FORM PTO-1449</b> (Rev. 2-32)	<b>U.S. Department of Commerce</b> <b>Patent and Trademark Office</b>	<b>Atty. Docket No.</b> 01-44	<b>Serial No.</b> 09/991,245
		<b>Applicant:</b> Sunil Mehta and Fabiano Fontana	
		<b>Filing Date:</b> November 14, 2001	<b>Group:</b> 2818 2824

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use several sheets if necessary)

**U.S. PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
HN	4 8 2 9 2 0 3	05/09/89	Ashmore, Jr.	307	469	04/20/88
HN	4 8 6 2 0 1 9	08/29/89	Ashmore, Jr.	307	469	04/20/88
HN	4 8 6 6 3 0 7	09/12/89	Ashmore, Jr.	307	469	04/20/88
HN	4 8 8 5 7 1 9	12/05/89	Brahmbhatt	365	181	08/19/87
HN	4 9 2 4 2 7 8	05/08/90	Logie	357	23.5	11/15/88
HN	5 2 7 0 5 8 7	12/14/93	Zagar	307	469	01/06/92
HN	6 0 2 8 7 8 9	02/22/00	Mehta et al.	365	185.14	06/15/99

**FOREIGN PATENT DOCUMENTS**

Document Number	Date	Country	Class	Subclass	Translation	
					Yes	No

**OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc).**

HN	Patent Application Serial No. 09/704,487 filed November 2, 2000, with title "Wide Input Programmable Logic System and Method" to Ravindar M. Lall, <i>now a US Patent No 6,507,212</i>
<b>EXAMINER</b> HIEN NGUYEN	<b>DATE CONSIDERED</b> 4/16/03

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.